Multi output SAW Oscillator (MOSO)

OUTPUT: LVDS





Product Number (please contact us) X1M000421xxxx00

## **MG7050VAN**

Feature

•Ultra Low jitter : 0.3 ps Max.

2 or 4 outputs and it is able to reduce fan-out buffers
 Frequency range
 Supply voltage
 External dimensions
 Output
 able to reduce fan-out buffers
 100 MHz to 700 MHz
 2.5 V / 3.3 V
 7.0 × 5.0 × 1.6 mm
 LVDS (2 or 4 outputs)

Application

Server, Storage, Network Instrument.





Actual size



#### Specifications (characteristics)

Openitorio (characteristics)					
Item	Symbol	Specifications		Conditions / Remarks	
		100 MHz to 700 MHz		Please contact us about available frequencies.	
Output frequency range	fo	100MHz, 106.25MHz, 125MHz, 150MHz, 156.25MHz, 200MHz, 212.5MHz, 250MHz, 300MHz, 312.5MHz		Standard frequency	
Supply voltage	Vcc	D: 2.5 V ± 0.125 V	C: 3.3 V ± 0.33 V	Vcc,Vcc1 and Vcc2 need same voltage	
Storage temperature	T_stg	-55 °C to +125 °C		Store as bare product after packing	
Operating temperature	T_use	A: 0 °C to +70 °C, B: -20 °C to +70 °C D: -5 °C to +85 °C			
Frequency tolerance *1	f_tol	J: ±50 × 10 <sup>-6</sup> ,	L: ±100 × 10 <sup>-6</sup>		
Current consumption	loo	35 mA Typ., 50 mA Max.	45 mA Typ., 56 mA Max.	2-outputs	OF-Vac I IVDS-100 O
Current consumption	Icc	40 mA Typ., 66 mA Max.	50 mA Typ., 72 mA Max.	4-outputs	OE=Vcc, L_LVDS=100 Ω
Disable current	I_dis	7 mA Typ., 18 mA Max.	8 mA Typ., 20 mA Max.	OE=GND	
Symmetry	SYM	45 % t	o 55 %	At outputs crossing point	
Output valtage	V <sub>OD</sub>	247 mV to 454 mV		DC characteristics	
Output voltage	Vos	1.125 V to 1.375 V			
Output load condition	L_LVDS	100 Ω		Connected between OUTnP and OUTnN	
Input voltage	$V_{IH}$	70% V <sub>CC</sub> Min.		OE and FSEL terminals	
Input voltage	$V_{IL}$	30% V <sub>CC</sub> Max.			
Rise time / Fall time	tr/tf	200 ps Typ., 400 ps Max.		Between 20% and 80 to peak voltage	0% of differential output peak
Start-up time	t_str	5 ms Typ., 10 ms Max.		Time at minimum supply voltage to be 0 s	
		0.19 ps Typ.	0.16 ps Typ.	fo=100 MHz	
	tPJ	0.18 ps Typ.	0.15 ps Typ.	fo=125 MHz	Offset frequency: 12 kHz to 20 MHz
		0.17 ps Typ.	0.14 ps Typ.	fo=156.25 MHz	
Phase Jitter		0.15 ps Typ.	0.13 ps Typ.	fo=212.5 MHz	
		0.12 ps Typ.	0.11 ps Typ.	fo=312.5 MHz	
		0.06 ps Typ.	0.05 ps Typ.	fo=700 MHz	
		0.3 ps Max.			
Skew	t_skew	20 ps Typ., 50 ps Max.		FSEL=H	
Aging	f_age	N: ±10 × 10 <sup>-6</sup> / year Max.		First year	+25 °C, Vcc=2.5 V, 3.3 V
Aging		A: Included in Fred	quency tolerance *2	10 years	+25 °C, VCC=2.5 V, 3.3

<sup>\*1</sup> Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change and reflow drift.

Product Name (Standard form)

(@@9:JDA, JBA are not available)

①Model

②Output (L: LVDS)

③Frequency

⑤"A": Fixed⑥Supply voltage⑦Frequency tolerance⑥Operating temperature⑨Frequency aging

Supply voltage		
С	3.3 V Typ.	
D	2.5 V Typ.	

⑦Frequency tolerance		
J	±50 × 10 <sup>-6</sup>	
L	±100 × 10 <sup>-6</sup>	

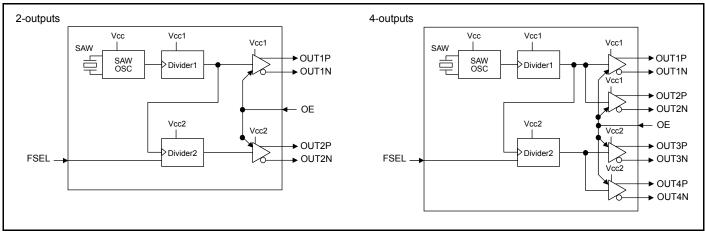
®Operating temp.		
Α	0 to +70°C	
В	-20 to +70°C	
D	-5 to +85°C	

Α	Frequency tolerance include aging		
Ν	Frequency tolerance exclude aging		

<sup>\*2 &</sup>quot;A" is not acceptable when Frequency tolerance is "J" and Operating temperature is "B" or "D".



#### Block diagram



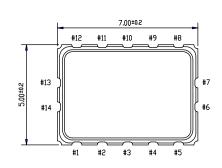
#### FSEL function

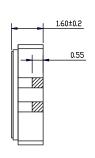
2-outputs		OUT1 OUT2		
4-outputs		OUT1 / OUT2	OUT3 / OUT4	
FSEL	Н	fo	fo	
	L	fo	fo/2	

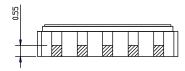
#### External dimensions

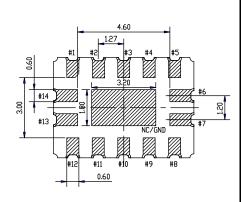
#### (Unit :mm)

#### Footprint (Recommended) (Unit :mm)





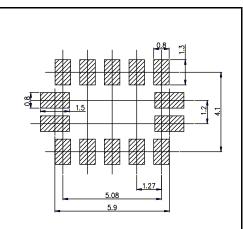




OE pin = "H" : Specified frequency output. OE pin = "L" : Output is high impedance #14 is connected to the cover.

Pin	Connection		
	2-outputs	4-outputs	
1	V <sub>cc</sub> 1		
2	GND	OUT1P	
3	OUT1P	OUT1N	
4	OUT1N	OUT2P	
5	GND	OUT2N	
6	FSEL		
7	OE		
8	GND	OUT3N	
9	OUT2N	OUT3P	
10	OUT2P	OUT4N	
11	GND	OUT4P	
12	V <sub>CC</sub> 2		
13	V <sub>CC</sub>		
14	GND		

Connection



To maintain stable operation, provide a 0.01  $\mu F$  to 0.1  $\mu F$  by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between  $V_{CC},\,V_{CC}1,\,V_{CC}2$ - GND).

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

    (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



 $\blacktriangleright$  Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc ).

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